B.Tech / Integrated (MBA/M.Tech) 4th Semester Examination
June 2014
Computer Architecture and Organization
Subject Code: CSL-202

Time Allowed: 03 hours. Maximum Marks: 100

Before answering the question paper the candidate should ensure that they have been supplied the correct question paper. Complaints in this regard, if any, shall not be entertained after the examination.

Note: Question No. 1 is Compulsory and attempt two questions from each section. All questions carry equal marks.

1. a) Explain different types of computer architecture with diagram.
   b) Write short notes on control memory.
   c) Differentiate between RISC and CICS types of processors.
   d) Write a short note on cache coherence. [5 x 4 = 20]

SECCTION A

2. a) Explain common bus system in detail. [10 x 2 = 20]
   b) What is store program concept? Explain with example?

3. Explain the design of control unit. What is the main task of microprogram sequencer? [20]

4. a) What are the various generic instruction address format. Explain with the help of given example.
   \[ Z = D*U-Z*X \] [10 x 2 = 20]
   b) What are the types of CPU organization? Explain Stack based CPU organization.

SECTION B

5. a) Define Amdahl’s law. [10 x 2 = 20]
   b) What are the different Flynn’s classifications of computers?

6. a) Define associative memory. Explain its hardware organization. [10 x 2 = 20]
   b) Discuss the configuration of RAM and ROM chips.

7. Explain in details all different modes of transfer to and from peripherals. [20]